EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S34	9	S33 AND (simulat$3 emulat$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/30 14:55
S33	32	S32 AND (switch near fabric) AND ((chip adj set) chip$1set)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/30 14:54
S32	450	payload AND (cell WITH (size sized sizing)) AND ((chip micro $1processor "micro processor" ASIC "application specific integrated circuit" application $1specific$1integrated$1circuit IC "integrated circuit" integrated$1circuit co $1processor "co processor" (integrated adj circuit) (digital adj signal adj processor)) with (version versioning revision revisioning batch ID identification identify identified identifying tag tagged tagging Mark Marked Marking label labeled labelled labeling labelling name named naming model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/30 14:53
S31	11	S24 not S25	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:20
S30	10	S29 AND payload	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:05

S29	10	S28 not S24	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:05
S28	11	S27 AND ((chip micro $1processor "micro processor" ASIC "application specific integrated circuit" application $1specific$1integrated$1circuit IC "integrated circuit" integrated$1circuit co $1processor "co processor" (integrated adj circuit) (digital adj signal adj processor)) with (version versioning revision revisioning batch ID identification identify identified identifying tag tagged tagging Mark Marked Marking label labeled labelled labeling labelling name named naming model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:05
S27	58	emulat$3 AND (switch near fabric) AND ((chip adj set) chip $1set)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:04
S26	0	emulat$3 SAME (switch near fabric) SAME ((chip adj set) chip $1set)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:04
S25	19	S24 AND payload	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:03

S24	30	S22 AND ((chip micro $1processor "micro processor" ASIC "application specific integrated circuit" application $1specific$1integrated$1circuit IC "integrated circuit" integrated$1circuit co $1processor "co processor" (integrated adj circuit) (digital adj signal adj processor)) with (version versioning revision revisioning batch ID identification identify identified identifying tag tagged tagging Mark Marked Marking label labeled labelled labeling labelling name named naming model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:03
S23	94	S22 AND (chip micro $1processor "micro processor" ASIC "application specific integrated circuit" application $1specific$1integrated$1circuit IC "integrated circuit" integrated$1circuit co $1processor "co processor" (integrated adj circuit) (digital adj signal adj processor))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:03
S22	95	simulat$3 AND (switch near fabric) AND ((chip adj set) chip $1set)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:02
S21	1	simulat$3 SAME (switch near fabric) SAME ((chip adj set) chip $1set)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 23:02
S20	8	S16 AND ((chip micro $1processor "micro processor" ASIC "application specific integrated circuit" application $1specific$1integrated$1circuit IC "integrated circuit" integrated$1circuit co $1processor "co processor" (integrated adj circuit) (digital adj signal adj processor)) with (version versioning revision revisioning batch ID identification identify identified identifying tag tagged tagging Mark Marked Marking label labeled labelled labeling labelling name named naming model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 22:53

S19	12	S16 AND (chip micro $1processor "micro processor" ASIC "application specific integrated circuit" application $1specific$1integrated$1circuit IC "integrated circuit" integrated$1circuit co $1processor "co processor" (integrated adj circuit) (digital adj signal adj processor))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 22:52
S18	1	S16 AND (cell WITH (size sized sizing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 22:52
S17	3	S16 AND payload	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/29 22:43
S16	19	(US-20030038842-$ or US-20020186245-$ or US-20050015234-$ or US-20040267512-$ or US-20040102942-$ or US-20040181385-$ or US-20020173943-$ or US-20040024578-$ or US-20050177353-$ or US-20030212989-$).did. or (US-6996799-$ or US-6053947-$ or US-5862361-$ or US-5917731-$ or US-6134514-$ or US-6370494-$ or US-6466898-$ or US-7085703-$ or US-6442740-$).did.	US-PGPUB; USPAT	OR	ON	2010/07/29 22:43

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L13	0	12 AND (simulat$3 emulat$3).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:50
L12	12	11 not 9	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:26
L11	12	8 not 1	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:25

L10	0	7 AND ((chip adj set) chip$1set). clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:25
L9	2	7 AND (simulat$3 emulat$3).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:23
L8	13	7 AND (switch near fabric).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:22
L7	111	payload.clm. AND (cell WITH (size sized sizing)).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:22
L6	0	4 AND ((chip adj set) chip$1set). clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:19
L5	0	4 AND (switch near fabric).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:19
L4	241	payload.clm. AND (simulat$3 emulat$3).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:19
L3	0	payload.clm. AND (switch near fabric).clm. AND ((chip adj set) chip$1set).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:19
L2	1	(simulat$3 emulat$3).clm. AND (switch near fabric).clm. AND ((chip adj set) chip$1set).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:18
L1	3	payload.clm. AND (cell WITH (size sized sizing)).clm. AND ((chip micro$1processor "micro processor" ASIC "application specific integrated circuit" application$1specific$1integrated $1circuit IC "integrated circuit" integrated$1circuit co$1processor "co processor" (integrated adj circuit) (digital adj signal adj processor)).clm. with (version versioning revision revisioning batch ID identification identify identified identifying tag tagged tagging Mark Marked Marking label labeled labelled labeling labelling name named naming model).clm.)	US-PGPUB; USPAT; UPAD	OR	ON	2010/07/30 22:18

7/30/2010 10:51:15 PM
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